

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	10445	(flash eeprom nonvolatile (non adj volatile) flashrom)near7(embed\$ inclu\$4 implement\$)with(controller chip chipset single ic asic fpga)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 15:40
S2	3673	updat\$ near6(firmware bios boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 16:23
S3	11529	(flash eeprom nonvolatile (non adj volatile) flashrom)with(firmware bios boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 15:41
S4	254	S1 and S2 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 15:42
S5	111	S4 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 15:43
S6	1	"5647986".PN.	USPAT; USOCR	OR	ON	2005/06/08 16:00
S7	4743	(updat\$ upgrad\$)near6(firmware bios boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 16:24
S8	1046	low adj pin adj count	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 16:24
S9	69	S7 and S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 16:40
S10	28	(MARTWICK near2 ANDREW).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 16:40